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Appendix B. Standards compliance

The system conforms to the following industry standards.

Table 34. Industry standard documents

Standard	URL for information and specifications
ACPI Advance Configuration and Power Interface Specification, v2.0c	https://uefi.org/specsandtesttools
Ethernet IEEE 802.3-2005	https://standards.ieee.org/
HDG Hardware Design Guide Version 3.0 for Microsoft Windows Server	microsoft.com/whdc/system/platform/pcdesign/desguide/serverdg.mspx
IPMI Intelligent Platform Management Interface, v2.0	intel.com/design/servers/ipmi
DDR4 Memory DDR4 SDRAM Specification	jedec.org/standards-documents/docs/jesd79-4.pdf
PCI Express PCI Express Base Specification Rev. 2.0 and 3.0	pcisig.com/specifications/pciexpress
PMBus Power System Management Protocol Specification, v1.2	http://pmbus.org/Assets/PDFS/Public/PMBus_Specification_Part_I_Rev_1-1_20070205.pdf
SAS Serial Attached SCSI, v1.1	http://www.t10.org/
SATA Serial ATA Rev. 2.6; SATA II, SATA 1.0a Extensions, Rev. 1.2	sata-io.org
SMBIOS System Management BIOS Reference Specification, v2.7	dmtf.org/standards/smbios
TPM Trusted Platform Module Specification, v1.2 and v2.0	trustedcomputinggroup.org
UEFI Unified Extensible Firmware Interface Specification, v2.1	uefi.org/specifications
USB Universal Serial Bus Specification, Rev. 2.0	usb.org/developers/docs

Appendix C Additional resources

Table 35. Additional resources

Resource	Description of contents	Location
Installation and Service Manual	This manual, available in PDF format, provides the following information: • Chassis features • System Setup program • System messages • System codes and indicators • System BIOS • Remove and replace procedures • Troubleshooting • Diagnostics • Jumpers and connectors	Dell.com/Support/Manuals
Getting Started Guide	This guide ships with the system, and is also available in PDF format. This guide provides the following information: • Initial setup steps • Key system features • Technical specifications	Dell.com/Support/Manuals
Rack Installation Instructions	This document ships with the rack kits, and provides instructions for installing a server in a rack.	Dell.com/Support/Manuals
Information Update	This document ships with the system, is also available in PDF format online, and provides information on system updates.	Dell.com/Support/Manuals
System Information Label	The system information label documents the system board layout and system jumper settings. Text is minimized due to space limitations and translation considerations. The label size is standardized across platforms.	Inside the system chassis cover
Quick Resource Locator (QRL)	This code on the chassis can be scanned by a phone application to access additional information and resources for the server, including videos, reference materials, service tag information, and Dell EMC contact information.	Inside the system chassis cover
Energy Smart Solution Advisor (ESSA)	The Dell EMC online ESSA enables easier and more meaningful estimates to help you determine the most efficient configuration possible. Use ESSA to calculate the power consumption of your hardware, power infrastructure, and storage.	Dell.com/calc

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Appendix D. Support and deployment services

Topics:

- Dell EMC ProDeploy Enterprise Suite
- ProSupport Enterprise Suite
- ProSupport Plus
- ProSupport
- ProSupport One for Data Center
- Support Technologies
- Additional professional services
- Dell Education Services
- Dell EMC Global Infrastructure Consulting Services
- Dell EMC Managed Services

Dell EMC ProDeploy Enterprise Suite

ProDeploy Enterprise Suite gets your server out of the box and into optimized production—fast. Our elite deployment engineers with broad and deep experience utilizing best-in-class processes along with our established global scale can help you around the clock and around the globe. From simple to the most complex server installations and software integration, we take the guess work and risk out of deploying your new server technology.

		Basic Deployment	ProDeploy	ProDeploy Plus
Pre-deployment	Single point of contact for project management		•	In-region
	Site readiness review		•	•
	Implementation planning		•	•
	Technology Service Manager (TSM) engagement for ProSupport Plus entitled devices			•
Deployment	Deployment service hours	Business hours	24x7	24x7
	Onsite hardware installation*	•	•	•
	Packaging materials disposal	•	•	•
	Install and configure system software		•	Onsite
	Project documentation with knowledge transfer		•	•
Post-deployment	Deployment verification		•	•
	Configuration data transfer to Dell EMC technical support		•	•
	30-days of post-deployment configuration assistance			•
	Training credits for Dell EMC Education Services			•

Figure 6. ProDeploy Enterprise Suite capabilities

NOTE: Hardware installation not applicable on selected software products.

Dell EMC ProDeploy Plus

From beginning to end, ProDeploy Plus provides the skill and scale needed to successfully execute demanding deployments in today's complex IT environments. Certified Dell EMC experts start with extensive environmental assessments and detailed migration planning and recommendations. Software installation includes set up of most versions of Dell EMC SupportAssist and OpenManage system management utilities. Post-deployment configuration assistance, testing, and product orientation services are also available.

Dell EMC ProDeploy

ProDeploy provides full service installation and configuration of both server hardware and system software by certified deployment engineers including set up of leading operating systems and hypervisors as well as most versions of Dell EMC SupportAssist and OpenManage system management utilities. To prepare for the deployment, we conduct a site readiness review and implementation planning exercise. System testing, validation, and full project documentation with knowledge transfer complete the process.

Dell EMC Basic Deployment

Basic Deployment delivers worry-free professional installation by experienced technicians who know Dell EMC servers inside and out.

ProSupport Enterprise Suite

With Dell EMC ProSupport Services, we can help you keep your operation running smoothly, so you can focus on running your business. We will help you maintain peak performance and availability of your most essential workloads. Dell EMC ProSupport is a suite of support services that enable you to build the solution that is right for your organization. Choose support models based on how you use technology and where you want to allocate resources. From the desktop to the data center, address everyday IT challenges, such as unplanned downtime, mission-critical needs, data and asset protection, support planning, resource allocation, software application management and more. Optimize your IT resources by choosing the right support model.

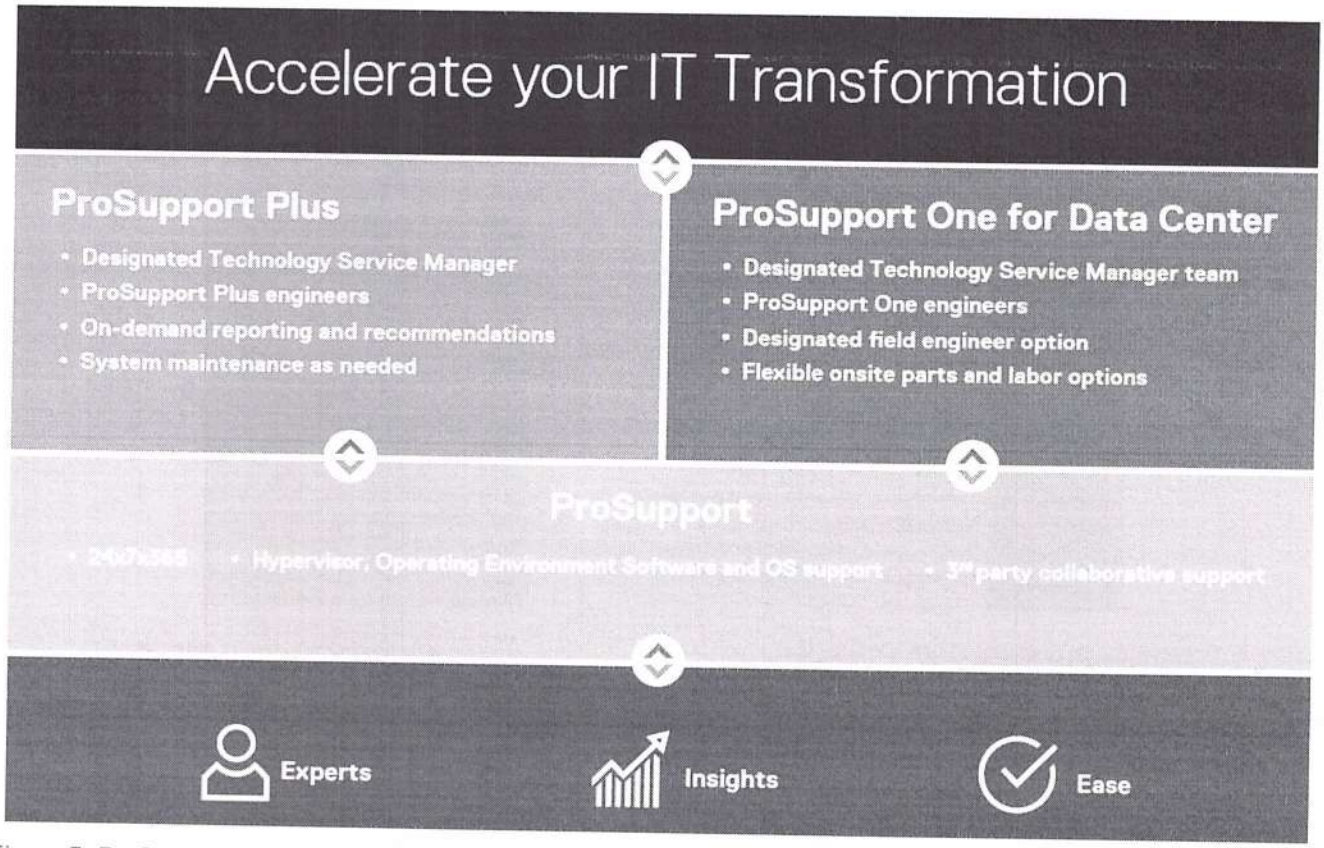


Figure 7. ProSupport Enterprise Suite

ProSupport Plus

When you purchase PowerEdge servers, we recommend ProSupport Plus, our proactive and preventative support, for business-critical systems. ProSupport Plus provides all the benefits of ProSupport, plus the following:

- An assigned Services Account Manager (SAM) who knows your business and your environment
- Access to senior ProSupport engineers for faster issue resolution
- Personalized, preventive recommendations based on analysis of support trends and best practices from across the Dell EMC customer base to reduce support issues and improve performance
- Predictive analysis for issue prevention and optimization enabled by SupportAssist
- Proactive monitoring, issue detection, notification and automated case creation for accelerated issue resolution enabled by SupportAssist
- On-demand reporting and analytics-based recommendations enabled by SupportAssist and TechDirect

ProSupport

Our ProSupport service offers highly trained experts around the clock and around the globe to address your IT needs. We will help you minimize disruptions and maximize availability of your PowerEdge server workloads with:

- 24x7x365 access to certified hardware and software experts
- Collaborative 3rd party support
- Hypervisor and OS support
- Consistent level of support available for Dell EMC hardware, software and solutions
- Onsite parts and labor response options including next business day or four-hour mission critical

ProSupport One for Data Center

ProSupport One for Data Center offers flexible site-wide support for large and distributed data centers with more than 1,000 assets. This offering is built on standard ProSupport components that leverage our global scale but are tailored to your company's needs. While not for everyone, it offers a truly unique solution for Dell EMC's largest customers with the most complex environments.

- Team of assigned Services Account Managers (SAM) with remote, on-site options
- Assigned ProSupport One technical and field engineers who are trained on your environment and configurations
- On-demand reporting and analytics-based recommendations enabled by SupportAssist and TechDirect
- Flexible on-site support and parts options that fit your operational model
- A tailored support plan and training for your operations staff

	ProSupport	ProSupport Plus	ProSupport One for Data Center
Remote technical support	24x7	24x7	24x7
Parts and labor response options	Next business day or Mission Critical	Next business day or Mission Critical	Flexible
Automated issue detection and case creation	●	●	●
Self-service case initiation and management	●	●	●
Hypervisor and OS support	●	●	●
Priority access to specialized support experts		●	●
Designated Technology Service Manager		●	●
Personalized assessments and recommendations		●	●
On-demand support and utilization reports		●	●
Systems Maintenance guidance		Semiannual	Optional
Designated technical and field support teams			●

Figure 8. Enterprise Support feature comparison

Support Technologies

Powering your support experience with predictive, data-driven technologies.

SupportAssist

The best time to solve a problem is before it happens. The automated proactive and predictive technology SupportAssist* helps reduce steps and time to resolution, often detecting issues before they become a crisis. Benefits include:

- Value - SupportAssist is available to all customers at no additional charge.
- Improve productivity - replace manual, high-effort routines with automated support.
- Accelerate time to resolution - receive issue alerts, automatic case creation and proactive contact from Dell EMC experts.
- Gain insight and control - optimize enterprise devices with on-demand ProSupport Plus reporting in TechDirect and get predictive issue detection before the problem starts.

SupportAssist is included with all support plans but features vary based on service level agreement.

	Basic Hardware Warranty	ProSupport	ProSupport Plus
Automated issue detection and system state information collection	•	•	•
Proactive, automated case creation and notification		•	•
Predictive issue detection for failure prevention			•
Recommendation reporting available on-demand in TechDirect			•

Figure 9. SupportAssist model

Get started at Dell.com/SupportAssist

TechDirect

Boost your IT teams productivity when supporting Dell EMC systems. With over 1.4 million self-dispatches processed each year, TechDirect has proven its effectiveness as a support tool. You can:

- Self-dispatch replacement parts
- Request technical support
- Integrate APIs into your help desk

Or, access all your Dell EMC certification and authorization needs. Train your staff on Dell EMC products as TechDirect allows you to:

- Download study guides
- Schedule certification and authorization exams
- View transcripts of completed courses and exams

Register at techdirect.dell.com

Additional professional services

Dell Education Services

Dell Education Services offers the PowerEdge server training courses designed to help you achieve more with your hardware investment. The curriculum is designed in conjunction with the server development team, as well as Dell EMC's technical support team, to ensure that the training delivers the information and practical, hands-on skills you and your team need to confidently manage and maintain your Dell EMC server solution. To learn more or register for a class today, visit LearnDell.com/Server.

Dell EMC Global Infrastructure Consulting Services

Dell EMC Global Infrastructure Consulting Services use skilled solution architects, innovative tools, automated analysis and Dell EMC's intellectual property to give rapid insight into the root causes of unnecessary complexity. We seek better answers than traditional service models, and our strategy is to help quickly identify high-impact, short-duration projects that deliver return on investment (ROI) and free up resources. The results are practical, action-oriented plans with specific, predictable, measurable outcomes. From data center optimization to server virtualization to systems management, our consulting services can help build a more efficient enterprise.

Dell EMC Managed Services

Dell EMC Managed Services are a modular set of lifecycle services designed to help you automate and centrally configure, deploy, and manage your day-to-day data center operations. These services extend your existing on-premise IT infrastructure with off-premise cloud services designed to better address challenges with mobility, highly distributed organizations, security, compliance, business continuity, and disaster preparedness.

SPEC CPU®2017 Integer Rate Result

Dell Inc.

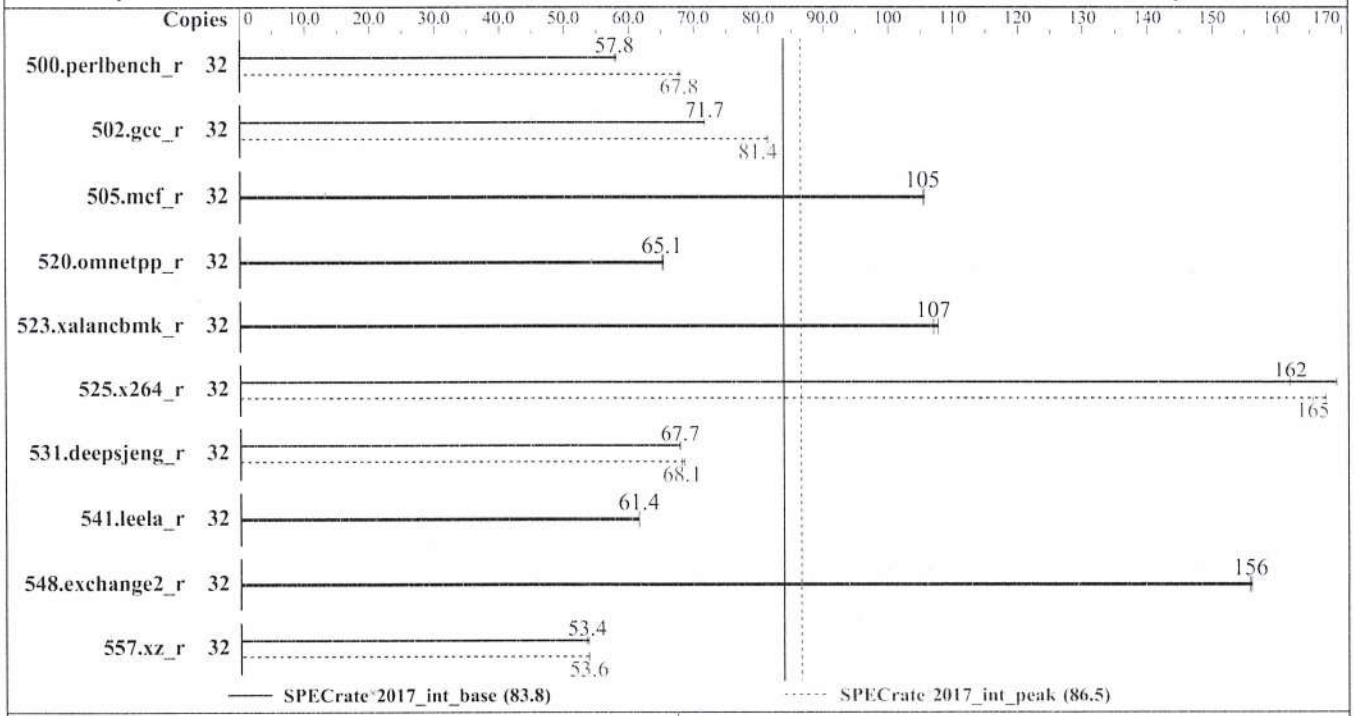
PowerEdge T440 (Intel Xeon Silver 4208, 2.10 GHz)

SPECrate®2017_int_base = 83.8

SPECrate®2017_int_peak = 86.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: Nov-2019



Hardware

CPU Name:	Intel Xeon Silver 4208
Max MHz:	3200
Nominal:	2100
Enabled:	16 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	11 MB I+D on chip per chip
Other:	None
Memory:	384 GB (12 x 32 GB 2Rx8 PC4-2933V-R, running at 2933)
Storage:	1 x 1.92 TB SATA SSD
Other:	None

Software

OS:	Red Hat Enterprise Linux 8.1 kernel 4.18.0-147.el8.x86_64
Compiler:	C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux
Parallel:	No
Firmware:	Version 2.5.4 released Jan-2020
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	None jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge T440 (Intel Xeon Silver 4208, 2.10 GHz)

SPECrate®2017_int_base = 83.8

SPECrate®2017_int_peak = 86.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: Nov-2019

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	**881**	**57.8**	877	58.1			32	**752**	**67.8**	749	68.0		
502.gcc_r	32	632	71.7	**632**	**71.7**			32	557	81.4	**557**	**81.4**		
505.mcf_r	32	490	106	**491**	**105**			32	490	106	**491**	**105**		
520.omnetpp_r	32	643	65.3	**645**	**65.1**			32	643	65.3	**645**	**65.1**		
523.xalancbmk_r	32	314	108	**316**	**107**			32	314	108	**316**	**107**		
525.x264_r	32	**346**	**162**	331	169			32	**339**	**165**	335	167		
531.deepsjeng_r	32	**541**	**67.7**	541	67.8			32	**539**	**68.1**	536	68.4		
541.leela_r	32	**863**	**61.4**	863	61.4			32	**863**	**61.4**	863	61.4		
548.exchange2_r	32	**538**	**156**	538	156			32	**538**	**156**	538	156		
557.xz_r	32	**648**	**53.4**	644	53.7			32	**644**	**53.6**	644	53.7		

SPECrate®2017_int_base = **83.8**

SPECrate®2017_int_peak = **86.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
      "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-
      32"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 1x Intel Core i9-9900K CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

(Continued on next page)

General Notes (Continued)

```
is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
UPI Prefetch enabled
LLC Prefetch disabled
Dead Line LLC Alloc enabled
Directory AtoS disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on localhost.localdomain Thu Apr 30 12:27:07 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
      2  "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 8
```

(Continued on next page)

Platform Notes (Continued)

```
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7

 From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                32
      On-line CPU(s) list:   0-31
      Thread(s) per core:    2
      Core(s) per socket:    8
      Socket(s):             2
      NUMA node(s):          2
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 85
      Model name:            Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
      Stepping:              6
      CPU MHz:               1206.838
      CPU max MHz:           3200.0000
      CPU min MHz:           800.0000
      BogoMIPS:              4200.00
      Virtualization:        VT-x
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              1024K
      L3 cache:              11264K
      NUMA node0 CPU(s):     0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
      NUMA node1 CPU(s):     1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
     aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
     xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
     avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
     invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
     flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
     cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
     avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
     cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
     arch_capabilities

 /proc/cpuinfo cache data
    cache size : 11264 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
```

(Continued on next page)

Platform Notes (Continued)

```
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
  node 0 size: 192074 MB
  node 0 free: 191298 MB
  node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
  node 1 size: 193506 MB
  node 1 free: 192579 MB
  node distances:
  node   0   1
    0:  10  21
    1:  21  10

From /proc/meminfo
   MemTotal:       394835328 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.1 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.1"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

uname -a:
   Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):        Not affected
Microarchitectural Data Sampling:         Not affected
CVE-2017-5754 (Meltdown):                 Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                           via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):        Mitigation: usercopy/swapgs barriers and __user
                                           pointer sanitization
CVE-2017-5715 (Spectre variant 2):        Mitigation: Enhanced IBRS, IBPB: conditional,
                                           RSB filling
```

(Continued on next page)

Platform Notes (Continued)

```
 run-level 3 Apr 30 12:21 last=5

 SPEC is set to: /home/cpu2017
    Filesystem           Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   1.7T   20G  1.7T   2% /home

 From /sys/devices/virtual/dmi/id
     BIOS:    Dell Inc. 2.5.4 01/14/2020
     Vendor:  Dell Inc.
     Product: PowerEdge T440
     Product Family: PowerEdge
     Serial:  FBLH613

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     4x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
     8x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
     4x Not Specified Not Specified

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 19.0.5 NextGen
  Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.0.5
  NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
```

(Continued on next page)

Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 19.0.5 NextGen
  Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.0.5
  NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 19.0.5 NextGen
  Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.0.5
  NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
        | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 19.0.5
  NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64

(Continued on next page)

Base Portability Flags (Continued)

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -flto -mfpmath=sse
-funroll-loops -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -flto -mfpmath=sse
-funroll-loops -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib
-ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -flto -O3
-ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

(Continued on next page)

Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: -m64 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-04-30 13:27:07-0400.
Report generated on 2020-05-26 14:49:38 by CPU2017 PDF formatter v6255.
Originally published on 2020-05-26.

BROADCOM®

Data Sheet

BCM5720-2P

Dual-Port Ethernet Server Adapter

General Description

The Broadcom® NetXtreme® BCM5720-2P is a dual-port 10/100/1000 Mb/s x1 PCI Express (PCIe) Ethernet adapter that supports the IEEE 802.3ab standard over Category 5 twisted-pair cable. The adapter card supports offload technologies including Large Send, TCP segmentation, and TCP/UDP/IP checksum, and receive-side scaling (RSS) that deliver optimal network throughput, lower host processor utilization and as a result improve system overall performance. The adapter card comes in small form factor that is suitable for both low-profile and standard chassis.

Features

- Dual-port Gigabit Ethernet (GbE) Adapter
- IEEE 802.3ab 10/100/1000 Mb/s Gigabit Ethernet-compliant
- Virtual LANs (VLANs): IEEE 802.1q VLAN tagging
- x1 PCI Express v1.x and v2.x
- Energy Efficient Ethernet-compliant with IEEE Standard 802.3az-2010
- MSI and MSI-X capabilities—up to 17 MSI-X vectors
- I/O virtualization support for VMWare, NetQueue, and Microsoft VMQ
  - 17 receive queues and 16 transmit queues per port
  - 17 MSI-X vectors supporting per queue interrupt to host
- Function Level Reset (FLR)
- ECC error detection and correction on internal SRAM
- TCP, IP, and UDP checksum offload
- Large Send offload, TCP segmentation offload
- Receive-side scaling
- Jumbo frames (9 KB)
- IEEE 802.3x flow control
- Statistics for SNMP MIB II and Ethernet-like MIB
- Comprehensive diagnostic and configuration software suite
- ACPI 1.1a-compliant: multiple power modes
- Wake-on-LAN (WOL)
- Dual RJ-45 with integrated link and activity LEDs
- Low-profile form factor: 2.7 in. x 4.4 in.
- RoHS-compliant (lead-free)

Applications

The BCM5720-2P is compatible with x86 and x64 servers utilizing the PCIe v1.X and v2.X interfaces.

Figure 1: Functional Block Diagram

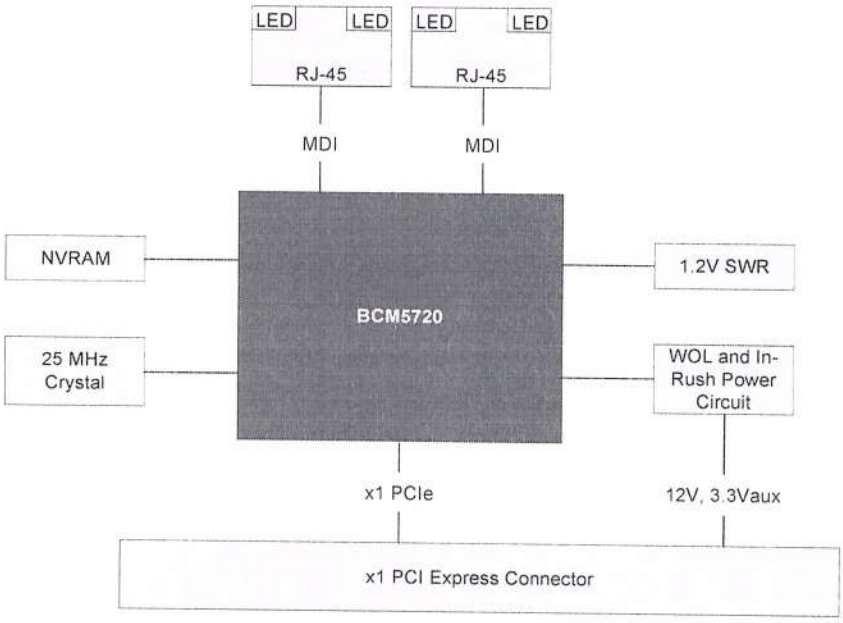


Table of Contents

Chapter 1: Introduction

1.1 Overview

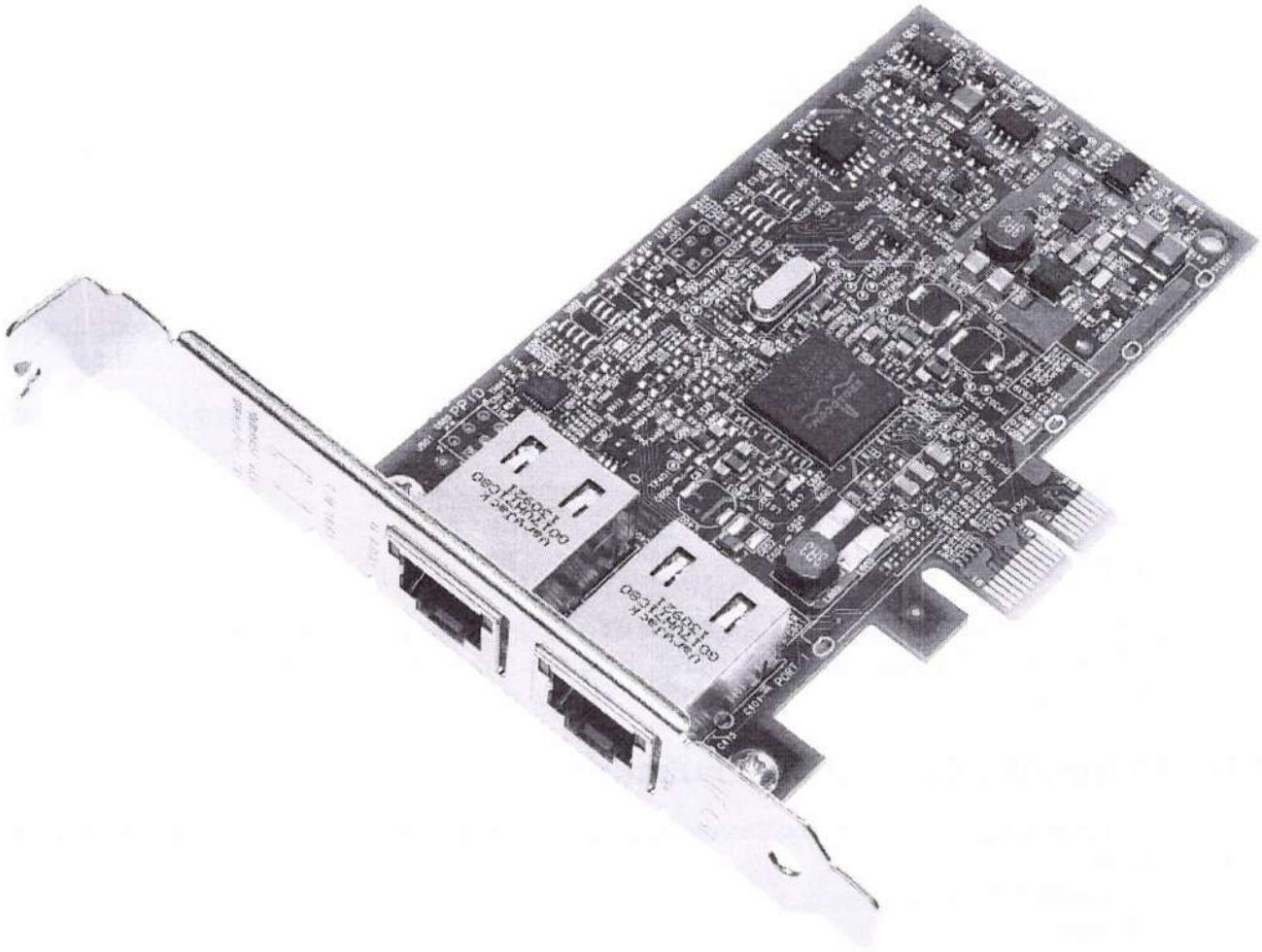
The Broadcom® BCM5720-2P is a dual-port 10/100/1000 Mb/s x1 PCI Express Specification v2.1 Ethernet server adapter that supports the IEEE 802.3ab standard over Category 5 twisted-pair cable. The adapter card supports offload technologies and receive-side scaling. Offload technologies (including Large Send, TCP segmentation, and TCP/UDP/IP checksum) and RSS deliver optimal network throughput and lower host processor utilization, resulting in improved overall system performance. The server adapter comes in a small form factor that is suitable for both low-profile and standard chassis configurations.

1.2 Features

The BCM5720-2P NIC supports the following features:

- Dual-port Gigabit Ethernet Server adapter
- IEEE 802.3ab 10/100/1000 Mb/s Gigabit Ethernet-compliant
- Virtual LANs (VLANs): IEEE 802.1q VLAN tagging
- x1 PCI Express v2.1
- Energy Efficient Ethernet compliant with IEEE Standard 802.3az-2010
- MSI and MSI-X capabilities—up to 17 MSI-X vectors
- I/O virtualization support for VMWare, NetQueue, and Microsoft VMQ
  - 17 receive queues and 16 transmit queues per port
  - 17 MSI-X vectors supporting per queue interrupt to host
- Function Level Reset (FLR)
- ECC error detection and correction on internal SRAM
- TCP, IP, and UDP checksum offload
- Large Send offload, TCP segmentation offload
- Receive-side scaling
- Jumbo frames (9 KB)
- IEEE 802.3x flow control
- Statistics for SNMP MIB II and Ethernet-like MIB
- Comprehensive diagnostic and configuration software suite
- ACPI 1.1a-compliant: multiple power modes
- Wake-on-LAN (WOL)
- Dual RJ-45 with integrated link and activity LEDs
- Low-profile form factor: 2.7" x 4.4"
- RoHS-compliant (lead-free)

Figure 2: BCM5720-2P Ethernet Server Adapter



Chapter 2: Functional Overview

2.1 Functional Block Diagram

The functional block diagram in Figure 1 shows the high-level components used in the BCM5720-2P. Each component is described in detail in the sections that follow.

2.2 PCI Express Host Interface

The BCM5720-2P Ethernet server adapter complies with the *PCI Express Base Specification* (Revisions 2.1) and supports 5.0 GT/s and 2.5 GT/s signaling capability.

The card-edge connector follows the x1 PCI Express standard pinout for add-in cards outlined in Table 5-1, Section 5.1 of *PCI Express Card Electromechanical Specification* (Revisions 2.0).

NOTE: For full network performance on both Ethernet ports, use 5.0 GT/s signaling.

2.3 Ethernet Server Adapter

The BCM5720-2P Ethernet server adapter is a dual-port Gigabit MAC with integrated PHY whose Gigabit Media Dependent Interface (MDI) differential signals are connected to the RJ-45 connectors. The BCM5720-2P controls the WOL power switching circuitry as well.

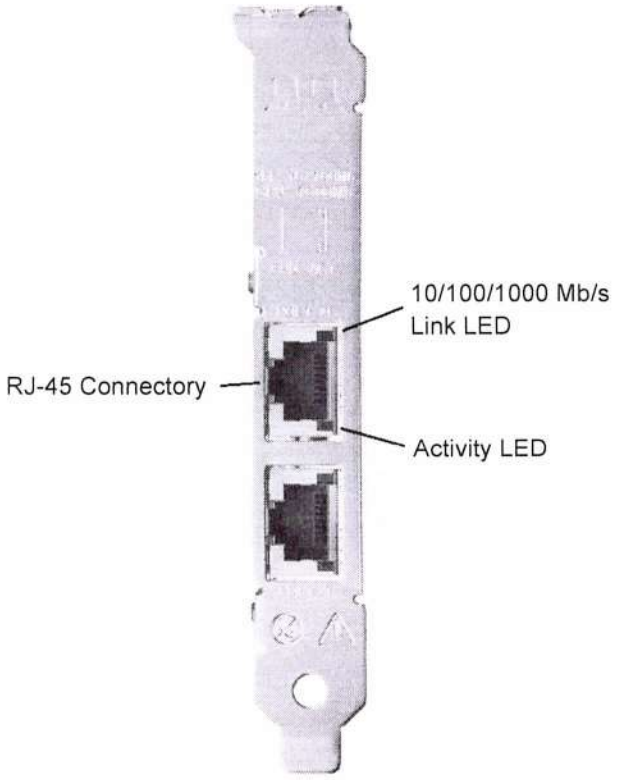
2.4 RJ-45 Connectors and LED Functions

The BCM5720-2P supports two RJ-45 connectors. There are two LEDs integrated on each RJ-45 connector. The LEDs are described as follows:

- The Link LED is located at the upper right corner of the connector.
  - Yellow = 10 Mb/s
  - Yellow = 100 Mb/s
  - Green = 1000 Mb/s
- The Activity LED is located at the lower right corner of the connector.
  - Green = Blinks when there is activity on the wires

Figure 3 shows the locations of the LEDs and the RJ-45 connectors.

Figure 3: LEDs and RJ-45 Connectors



2.5 Non-Volatile RAM

The BCM5720-2P uses an external nonvolatile serial flash memory (NVRAM) to store boot code and PCI configuration information such as Device ID and Vendor ID, as well as various firmware components. The boot code is downloaded to the device memory and is executed by an internal processor. User mode software running on the host system can be used to upgrade the boot code contents in the NVRAM.

2.6 Regulatory and Safety

The following sections detail the Regulatory, Safety, Electromagnetic Compatibility (EMC), and Electrostatic Discharge (ESD) standard compliance for the BCM5720-2P Network Interface Card.

2.6.1 Regulatory

Table 1: Regulatory Approvals

Item	Applicable Standard	Approval (A)/Certificate (C)
CE/European Union	EN 62368-1:2014	CB report and certificate
UL/USA	IEC 62368-1 (ed. 2)	CB report and certificate

2.6.2 Safety

Table 2: Safety Approvals

Country	Certification Type/Standard	Compliance
International	CB Scheme ICES 003 – Digital Device UL 1977 (connector safety) UL 796 (PCB wiring safety) UL 94 (flammability of parts)	Yes

2.6.3 Electromagnetic Compatibility (EMC)

Table 3: Electromagnetic Compatibility

Standard/Country	Certification Type	Compliance
CE/EU	EN 55032:2012/AC:2013 Class B EN 55024:2010 EN 61000-3-2:2014 EN 61000-3-3:2013	CE report and CE DoC
FCC/USA	CFR47 Part 15 Subpart B Class B	FCC/IC DoC and EMC report referencing FCC and IC standards
IC/Canada	ICES-003 Class B	FCC/IC DoC and report referencing FCC and IC standards
ACA/Australia, New Zealand	AS/NZS CISPR 22:2009 +A1 :2010	ACA certificate
BSM/Taiwan	CNS 13438 (2006) Class B	BSMI certificate
BSMI/Taiwan	CNS 15663	BSMI certificate/RoHS table
MSIP/S. Korea	RRL KN22 Class B KN24	Korea certificate MSIP mark
VCCI/Japan	VCCI V-3 (2015-04)	Copy of VCCI online certificate

2.6.4 Electrostatic Discharge (ESD) Compliance

Table 4: ESD Compliance Summary

Standard	Certification Type	Compliance
EN 55024:2010 (EN 61000-4-2)	Air/Direct discharge	Yes

2.6.5 FCC Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Consult the dealer or an experienced radio/TV technician for help.

NOTE: Changes or modifications not expressly approved by the manufacture responsible for compliance could void the user's authority to operate the equipment.

2.7 DC Power Specification

Table 5 shows the total power consumption required from the 12V and 3.3Vaux PCIe card-edge connector pins.

Table 5: Board Power Consumption

Condition	3.3Vaux (A)	12V (A)	Total Power (W)
No Link (idle)	0	0.082	1.00
10BASE-T	0	0.108	1.31
100BASE-T	0	0.111	1.35
1000BASE-T	0	0.183	2.23
Low Power Mode (10BASE-T)	0.138	0	0.50
Low Power Mode (100BASE-T)	0.161	0	0.58

2.8 Environmental Specifications

Table 6: Environmental Specifications

Parameter	Condition
Operating Temperature	0°C–55°C
Air Flow Requirement (LFM)	0
Storage Temperature	–40°C– +65°C
Storage Humidity	5% to 95% non-condensing
Vibration and Shock	IEC 68, FCC Part 68.302, NSTA, 1A

Table 6: Environmental Specifications

Parameter	Condition
Electrostatic/Electromagnetic Susceptibility	EN 61000-4-2, EN 55024

Chapter 3: Ordering Information

Table 7: Ordering Information

Part Number	Description
BCM5720-2P	Dual-Port Single Ethernet Server Adapter
BCM5720-2PBLK	Dual-Port Single box with Ten Ethernet Server Adapters

Revision History

5720-2P-DS102; January 4, 2018

Updated:

- Regulatory and Safety

5720-2P-DS101-R; March 13, 2015

Updated:

- Ordering information

57202P-DS100-R; February 9, 2015

Initial release.